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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.98) | Application Number | | 10508879 |
| | Filing Date | | 2004-09-22 |
| | First Named Inventor | Alexander Krasin | |
| | Art Unit | 2836 | |
| | Examiner Name | Zeev Kitov | |
| | Attorney Docket Number | SC11823TC | |

| U.S.PATENTS | | | | | | |
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| | Filing Date | | 2004-09-22 | |
| | First Named Inventor | Alexander Krasin | | |
| | Art Unit | 2836 | | |
| | Examiner Name | Zeev Kitov | | |
| Attorney Docket Number | | SC11823TC | | |

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| <i>Z.k.</i> | 3 | TORRES et al; "Modular, Portable, and Easily Simulated ESD Protection Networks for Advanced CMOS Technologies" <i>no date</i> | <input type="checkbox"/> |
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